

REMARKS

By the foregoing amendments, Applicants amend the drawings and the specification. Applicants have also canceled claims 42-61, 70-81, and 90-99, and added new claims 100-112. Thus, claims 100-112 are pending in the application. Applicants respectfully request consideration of all pending claims in view of the foregoing amendments and the following remarks.

All Rejections Under 35 U.S.C. §103 Are Rendered Moot

The Office Action rejects claims 42-49, 51-59, 61, 70-78, 80, 81, 90-96, 98 and 99 under 35 U.S.C. § 103. Office Action items 5, 8, and 14. Applicants do not concede the appropriateness of the most recent rejections and reserve the right to argue the merits of the rejected claims in future prosecution. However, Applicants have elected to cancel those claims for the sole purpose of advancing prosecution, and respectfully submit that such cancellation has rendered the rejections moot. Withdrawal of the rejections under 35 U.S.C. § 103 is requested.

The New Claims Are In Condition For Allowance

Applicants have added new claims 100-112, and believe them to be in condition for allowance.

As a threshold matter, Applicants note that no new matter has been added in the new claims. For instance, new claims 100-104 relate generally to subject matter previously recited in claims 45-50. The feature of “a plurality of cache lines” recited in claim 100 is disclosed on page 19 line 7 to page 20 line 19. The feature of “page table entries” recited in claim 103 is disclosed in the specification at page 11 line 15 to page 12 line 3. In addition, the features recited in claims 104-112 are disclosed at pages 9 to 21 of the specification.

The Applicants respectfully submit that the new claims are allowable for at least the reason that the cited references, either separately or combined, fail to disclose all recited features.

With regard to new claims 100-102, for example, the Office Action notes that “the cited prior art does not teach Applicant’s replacement policy of a common priority scheme for a plurality of cache lines.” Office Action item 19 at page 7. New claim 100 recites this feature, and Applicants submit that claim 100 is allowable for at least this reason. Claims 101 and 102 are allowable for at least the reason that they depend from allowable claim 100.

With regard to claim 103, Applicants respectfully submit that the cited sections of U.S. Patent No. 5,926,189 to Kim (*Kim*), Office Action item 15 at page 6, at best disclose address translations, generally. For the reason that *Kim* fails to disclose at least the feature of “virtual-physical address translation,” and combinations thereof, as recited in claim 103, claim 103 is allowable. Applicants respectfully submit that claim 102 is also allowable for the additional reason that the cited sections of *Kim* fail to disclose at least the feature of “page table entries,” and combinations thereof, as recited in claim 103.

With regard to claim 104, the Office Action indicates that “the cited prior art does not disclose or render obvious the following limitations: in a graphics accelerator including a texture cache system operating in a pre-fetch mode, pre-fetching a set of texels if it is determined that the set of texels can fit into one half of the texture cache memory.” Office Action item 17 at page 6. Applicants respectfully submit that new claim 104 is allowable for at least the reason that it recites this feature.

Applicants believe that new claims 105-112 are also in condition for allowance.

CONCLUSION

All of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicants therefore respectfully request that the Examiner reconsider all presently outstanding rejections, and that they be withdrawn. Applicants further request that new claims 100-112 be allowed. The Examiner is invited to telephone the undersigned representative if an interview might be useful to place the application in better condition for allowance.

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SRO:krf

Enclosure: Appendix Showing Changes to the Specification
Request for Approval of Drawing Amendment (incl. mark-up of Fig. 2)

APPENDIX

Please replace the paragraph beginning on page 11, line 15 and ending on page 12, line 3 with the following paragraph:

--In order to accomplish this complex process, a very large number of texture values must be available to the graphics accelerator. In accordance with the present invention, a graphic driver responding to an application program may set up data structures (preferably in local memory 21) which describe a large portion of memory (system or local) in which particular texture maps may be stored. The kernel driver obtains page tables from the operating system and uses these to provide page table entries (PTE) 24 in each data structure giving the physical addresses of texture map data in memory (system or local). The kernel driver then provides a reference to allow the direct memory access (DMA) engine 19 to access the data structures and find the portion of memory in which the texture maps are stored in response to application commands. The kernel driver may store the page table[s] entries 24 in local memory 21 so that the DMA engine 19 may accomplish virtual-to-physical address translations locally.--

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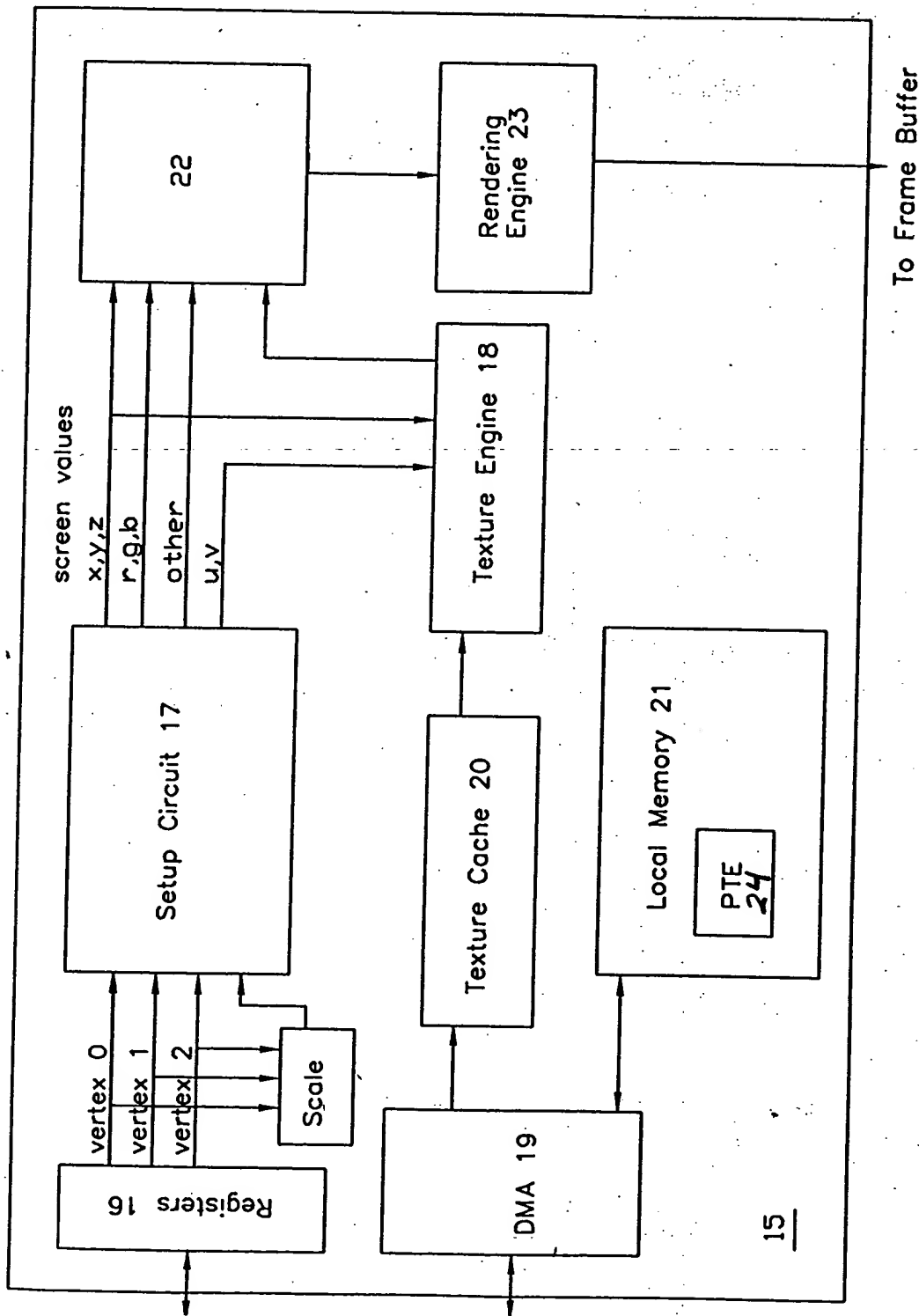


Figure 2